#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 1.53(b) Divisional Application of USSN: 09/920,927

Shinichiroh IKEMASU et al.

Group Art Unit: 2814 (Expected)

Serial No.: Unassigned

Examiner: Howard Weiss (Expected)

Filed: Herewith

For:

HIGHLY INTEGRATED AND RELIABLE DRAM AND ITS MANUFACTURE

Attorney Docket No.: 970607C

Customer No.: 38834

## INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

April 20,2004

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/920,927.

The Commissioner is authorized to charge our Deposit Account No. 50-2866 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

By:

Stephen G. Adrian Reg. No.: 32,878

Attorney for Applicant Tel: (202) 822-1100

Fax: (202) 822-1111

Attachment: PTO Form 1449

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## INFORMATION DISCLÖSURE CITATION PTO-1449

Atty. Docket No. 970607C

Serial No. Unassigned

Applicant(s): Shinichiroh IKEMASU et al.

Filing Date: April 20, 2004

Group Art Unit: 2814 (expected)

## **U.S. PATENT DOCUMENTS**

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
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Examiner

Date Considered

INFORMATION	Atty. Docket No. 970607C	Serial No. Unassigned	
DISCLOSURE CITATION	Applicant(s): Shinichiroh IKEMASU et al.		
PTO-1449	Filing Date: April 20, 2004	Group Art Unit: 2814 (expected)	

# OTHER DOCUMENTS

	AW	Tomihisa MIZUNO et al.; IEEE Transactions on Electron Devices, Vol. 38, No. 3, pp. 584-591; March 1991
Examiner		Date Considered